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STMICROELECTRONICS, INC. MAIL STATION 2346 1310 ELECTRONICS DRIVE CARROLLTON, TX 75006			RODRIGUEZ, GLENDA P	
			ART UNIT	PAPER NUMBER
			2651	

DATE MAILED: 01/26/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/993,869

Applicant(s)

OZDEMIR, HAKAN

Examiner

Glenda P. Rodriguez

Art Unit

2651

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 21 June 2004.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 1-35 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-7, 9-17 and 19-35 is/are rejected.
- 7) ☒ Claim(s) 8 and 18 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

Art Unit: 2651

## DETAILED ACTION

### *Claim Rejections - 35 USC § 102*

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims <sup>1-2, 29, 23 and 29-30</sup> ~~1-2, 29, 23 and 29-30~~ rejected under 35 U.S.C. 102(b) as being anticipated by Brown et al. (US Patent No. 4, 920, 434).

Regarding Claims 1 and 20, Brown et al. teach a servo circuit, comprising:

A servo channel operable to recover servo data from servo wedges that identify respective data sectors on a data-storage disk (Fig. 1, Elements 82, 70, 72 and 76 consists of a servo channel elements that produce positional servo information.); and

A processor coupled to the servo channel and operable to detect one of the servo wedges while the disk is attaining or after the disk attains an operating speed but before the servo channel recovers any servo data (Fig. 1, Element 76 and Col. 10, L. 12-34).

Claims (23 and 29) have limitations similar to those treated in the above rejection(s), and are met by the references as discussed above. Claim (23) however also recites the following limitations..."A data-storage disk having a surface, data sectors at respective locations of the surface, servo wedges that each include respective servo data that identifies the location of a respective data sector, and spin-up wedges that each include spin-up data and that are

Art Unit: 2651

respectively associated with some or all of the servo wedges, a motor coupled to and operable to rotate the disk (Fig. 1, Element 18); A read head operable to generate a read signal that represents the servo data and the spin-up data (Col. 3, L. 15-24 and Col. 10, L. 12-34. Brown et al. teach a microprocessor which accepts data read by the head during the spin-up operation (i.e. attaining the operating speed)), the read head having a position with respect to the surface of the data-storage disk (Col. 3, L. 24-38); A read-head positioning circuit operable to move the read head over the surface of the disk (Col. 3, L. 24-38)."

Method claims (30) is drawn to the method of using the corresponding apparatus claimed in claims (1 and 20). Therefore method claims (30) correspond to apparatus claims (1 and 20) and is rejected for the same reasons of obviousness as used above.

Regarding Claim 2, Brown et al. teach all the limitations of Claim 1. Brown et al. further teach wherein the processor is operable to cause the servo channel to recover servo data from the one servo wedge after the processor detects the one servo wedge and before the servo channel recovers servo data from any other servo wedge (Col. 10, L. 12-34).

### ***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claim 4 is rejected under 35 U.S.C. 103(a) as being unpatentable over Brown et al. in view of Tuttle (US Patent No. 6, 108, 151).

Regarding Claim 4, Brown et al. teach all the limitations of Claim 1. However, Brown et al. does not explicitly teach wherein teach the first servo wedge comprises a preamble and the processor is operable to detect the one servo wedge by detecting the preamble. Tuttle further teach the first servo wedge comprises a preamble and the processor is operable to detect the one servo wedge by detecting the preamble (Pat. No. 6, 108, 151; Fig. 2B and Col. 4, Lines 29-56). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify Brown et al.'s invention with the teaching of Tuttle in order to detect one of the servo wedges during or after disk spin-up search operation without first detecting a spin-up wedge in order to synchronize the timing recovery in the servo channel.

Regarding Claim 6, Brown et al. teach all the limitations of Claim 1. However, Brown et al. does not explicitly teach wherein the first servo wedge comprises a preamble and a servo synchronization mark. Tuttle teach the first servo wedge comprises a preamble and a servo synchronization mark following the preamble; the processor is operable to detect the first servo wedge by detecting the preamble; and the servo channel is operable to recover the synchronization mark in response to the processor detecting the preamble (Pat. No. 6, 108, 151; Fig. 2B and Col. 4, Lines 29-56). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify Brown et al.'s invention with the teaching of Tuttle to detect one of the servo wedges during or after disk spin-up search operation without first detecting a spin-up wedge in order to synchronize the timing recovery in the servo channel.

Regarding Claim 7, Brown et al. teach all the limitations of Claim 1. However, Brown et al. does not explicitly teach wherein the first and second servo wedges consists of a preamble and a synch mark. Tuttle et al. further teach the first servo wedge and a second servo wedge

Art Unit: 2651

following the first servo wedge each comprise a preamble and a servo synchronization mark following the preamble; the processor is operable to detect the one servo wedge by detecting the preamble of the one servo wedge; the servo channel is operable to recover the synchronization mark of the first servo wedge in response to the processor detecting the preamble of the first servo wedge; after detecting the first servo wedge, the processor is operable to detect the second servo wedge by detecting the preamble of the second servo wedge; and the servo channel is operable to recover the synchronization mark of the second servo wedge in response to the processor detecting the preamble of the second servo wedge (Pat. No. 6, 108, 151; Fig. 2B and Col. 4, Lines 29-56. It is obvious to a person of ordinary skill in the art to know that the servo channel during will read more than one servo wedge when the disk is in a spinning operation.). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify Brown et al.'s invention with the teaching of Tuttle to detect one of the servo wedges during or after disk spin-up search operation without first detecting a spin-up wedge in order to synchronize the timing recovery in the servo channel.

Regarding Claim 31, Brown et al. and Tuttle teach all the limitations of Claim 30. Tuttle et al. further teach wherein the second rotational speed is a steady-state speed or is approximately a steady-state speed (Pat. No. 6, 108, 151; Col. 15, Lines 13-15. It is obviously to a person of ordinary skill in the art to know that in a disk after performing a spin-up operation, it eventually detects synchronously at a stable speed). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to Brown et al.'s invention with the teaching of Tuttle in order to its initial velocity be a steady state velocity in order to detect the servo wedges.

5. Claims 3, 5, 9, 10-17, 21, 22, 24-28 and 32-35 are rejected under 35 U.S.C. 103(a) as being unpatentable over the combination of Brown et al. and Tuttle as applied to claim 1 above, and further in view of Leis et al. (US Patent No. 5, 036, 408).

Regarding Claim 3, the combination of Brown et al. and Tuttle et al. teach all the limitations of Claim 1. However, the combination does not teach wherein the first spin-up wedge comprises a zero-frequency field and the processor is operable to detect the first spin-up wedge by detecting the zero-frequency field. Leis et al. wherein it teaches a DC field placed in a sector (Pat. No. 5, 036, 408; See Abstract. It is known in the art that a dc or direct current signal has no frequency). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 5, the combination of Brown et al. and Tuttle teach all the limitations of Claim 1. However, the combination does not teach the first spin-up wedge comprises a zero-frequency field; the first servo wedge comprises a preamble that follows and that is contiguous with the zero-frequency field; and the processor is operable to detect the first spin-up wedge by detecting the zero-frequency field and is operable to detect the first servo wedge by detecting the preamble after detecting the zero-frequency field. Leis et al. wherein it teaches the first spin-up wedge comprises a zero-frequency field (Pat. No. 5, 036, 408, See Abstract); the first servo wedge comprises a preamble that follows and that is contiguous with the zero-frequency field (Pat. No. 5, 036, 408, See Abstract); and the processor is operable to detect the first spin-up wedge by detecting the zero-frequency field and is operable to detect the first servo wedge by

Art Unit: 2651

detecting the preamble after detecting the zero-frequency field (Pat. No. 5, 036, 408, See Abstract and Fig. 3A, Step 108). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claims 9 and 10, the combination of Brown et al. and Tuttle teach all the limitations of Claim 1. However, the combination does not teach the first spin-up wedge comprises a zero-frequency field; the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal; and the processor is operable to, compare samples of the read signal to a threshold, and detect the first spin-up wedge if a predetermined number of consecutive samples each have a predetermined relationship to the threshold. Leis et al. wherein it teaches teach the first spin-up wedge comprises a zero-frequency field (Pat. No. 5, 036, 408, See Abstract); the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal (Pat. No. 5, 036, 408, See Abstract and Col. 3, Lines 55-57, which indicates data is being sampled. Leis et al. teach a servo control system used for positioning the head. It is obvious to a person of ordinary skill in the art that servo channels are used for that same purpose.) and the processor is operable to, compare samples of the read signal to a threshold, and search and detect the first spin-up wedge if a predetermined number of consecutive samples each have a predetermined relationship to the threshold (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Fig. 3A, Step 104). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to



modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 11, the combination of Brown et al. and Tuttle teach all the limitations of Claims 1. However, the combination does not teach wherein the first spin-up wedge is disposed within the first servo wedge. Leis et al. wherein it teaches wherein the first spin-up wedge is disposed within the first servo wedge (Pat. 5, 036, 408; Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claims 12, 13, 14, 15, 16, 32, 33 and 34, the combination of Brown et al. and Tuttle teach all the limitations of Claims 1 and 30, respectively. However, the combination does not teach the first spin-up wedge comprises a zero-frequency field; the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal; and the processor is operable to, compare samples of the read signal to a threshold, and detect the first spin-up wedge if a predetermined number of consecutive samples each have a predetermined relationship to the threshold. Leis et al. wherein it teaches teach the first spin-up wedge comprises a zero-frequency field (Pat. No. 5, 036, 408, See Abstract); the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal (Pat. No. 5, 036, 408, See Abstract and Col. 3, Lines 55-57, which indicates data is being sampled. Leis et al. teach a servo control system used for positioning the head. It is obvious to a person of ordinary skill in the art that servo channels are used for that same

Art Unit: 2651

purpose.) and the processor is operable to, compare samples of the read signal to a threshold, and search and detect the preamble if a predetermined number of consecutive samples each have a predetermined relationship to the threshold (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Fig. 3A, Step 108). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 17, the combination of Brown et al. and Tuttle teach all the limitations of Claim 1. However, the combination does not teach the first spin-up wedge comprises a zero-frequency field; the first servo wedge comprises a preamble; the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal; and the processor is operable to, compare samples of the read signal to a threshold, search for the preamble of the first servo wedge in response to a first predetermined number of consecutive samples that have a predetermined relationship to the threshold, and abort the search for the preamble of the first servo wedge if the processor does not detect the preamble within a second predetermined number of consecutive samples that immediately follow the first predetermined number of consecutive samples. Leis et al. teach the first spin-up wedge comprises a zero-frequency field (Pat. No. 5, 036, 408, See Abstract); the first servo wedge comprises a preamble (Pat. No. 5, 036, 408, See Abstract); the servo channel is operable to generate a zero-frequency or approximately zero-frequency read signal that represents the zero-frequency field and to sample the read signal (Pat. No. 5, 036, 408, See Abstract and Col. 3, Lines 55-57, which indicates data is being sampled. Leis et al. teach a servo

Art Unit: 2651

control system used for positioning the head. It is obvious to a person of ordinary skill in the art that servo channels are used for that same purpose.); and the processor is operable to, compare samples of the read signal to a threshold, search for the preamble of the first servo wedge in response to a first predetermined number of consecutive samples that have a predetermined relationship to the threshold, and abort the search for the preamble of the first servo wedge if the processor does not detect the preamble within a second predetermined number of consecutive samples that immediately follow the first predetermined number of consecutive samples (Pat. No. 5, 036, 408, Fig. 3A and Col. 7, Lines 1-50 and Fig. 3A). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 21, the combination of Brown et al. and Tuttle teach all the limitations of Claim 20. However, the combination does not teach the first portion of the one servo wedge comprises a spin-up wedge; and the second portion of the servo wedge comprises a preamble. Leis et al. wherein it teaches the first portion of the one servo wedge comprises a spin-up wedge; and the second portion of the servo wedge comprises a preamble (Pat. No. 5, 036, 408; See Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 22, the combination of Brown et al. and Tuttle teach all the limitations of Claim 20. However, the combination does not teach wherein the processor is operable to

Art Unit: 2651

detect the first portion of the one servo wedge before detecting the second portion (Pat. No. 5, 036, 408, Fig. 3A and Col. 7, Lines 1-50 and Fig. 3A). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 24, the combination of Brown et al. and Tuttle teach all the limitations of Claim 23. However, the combination does not teach wherein the servo channel is operable to recover the servo data from the detected servo wedge; and the servo circuit is operable to, determine an initial position of the read head from the recovered servo data, and provide the initial position to the read-head positioning circuit. Leis et al. wherein it teaches the servo channel is operable to recover the servo data from the detected servo wedge (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract); and the servo circuit is operable to, determine an initial position of the read head from the recovered servo data, and provide the initial position to the read-head positioning circuit (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 25, the combination of Brown et al. and Tuttle teach all the limitations of Claim 23. However, the combination does not teach wherein the servo channel is operable to recover the servo data from the detected servo wedge and to provide the location of the respective data sector to the read-head positioning circuit. However, this feature is well known in the art as disclosed by Leis et al., wherein it teaches wherein the servo channel is operable to

Art Unit: 2651

recover the servo data from the detected servo wedge and to provide the location of the respective data sector to the read-head positioning circuit (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract. Leis et al. teach a servo control system used for positioning the head. It is obvious to a person of ordinary skill in the art that servo channels are used for that same purpose.). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 26, the combination of Brown et al. and Tuttle teach all the limitations of Claim 23. However, the combination does not teach wherein the servo channel is operable to recover the servo data from the detected servo wedge and to provide the location of the respective data sector to the read-head positioning circuit; and the read-head positioning circuit is operable to determine an initial position of the read head from the location of the respective data sector. However, this feature is well known in the art as disclosed by Leis et al., wherein it teaches the servo channel is operable to recover the servo data from the detected servo wedge and to provide the location of the respective data sector to the read-head positioning circuit (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract); and the read-head positioning circuit is operable to determine an initial position of the read head from the location of the respective data sector (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 27, the combination of Brown et al. and Tuttle teach all the limitations of Claim 23. However, the combination does not explicitly teach wherein the read-head position circuit and the servo circuit are unable to determine the position of the read head before the processor detects the one servo wedge. Leis et al. wherein it teaches the read-head position circuit and the servo circuit are unable to determine the position of the read head before the processor detects the one servo wedge (Pat. No. 5, 036, 408; See Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 28, the combination of Brown et al. and Tuttle teach all the limitations of Claim 23. However, the combination does not explicitly teach wherein the read head comprises a read-write head. Leis et al. wherein it teaches wherein the read head comprises a read-write head (Pat. No. 5, 036, 408; See Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

Regarding Claim 35, the combination of Brown et al. and Tuttle teach all the limitations of Claim 1. However, the combination does not explicitly teach wherein detecting the spin-up data and servo data comprises accurately detecting a predetermined number of spin-up wedges and servo wedges before recovering location identifying data and causing the recovered location identifying data to determine the circumferential position of the read head. Leis et al. teaches detecting DC erase fields before detecting the preamble in order to search for the

Art Unit: 2651

position on the head (Pat. No. 5, 036, 408; Col. 7, Lines 1-50 and Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to control the position and synchronize the read/write head (Pat. No. 5, 036, 408; See Abstract).

6. Claim 19 is rejected under 35 U.S.C. 103(a) as being unpatentable over Brown et al. and Tuttle as applied to claim 1, above, and further in view of Patapoutian et al. (US Patent No. 5, 661, 760).

The combination of Brown et al. and Tuttle teach all the limitations of Claim 1. However, the combination does not explicitly teach wherein the servo wedge comprises a predetermined binary sequence having groups of no more and no fewer than a predetermined number of consecutive bits each having a first logic level, the groups separated from each other by respective bits having a second logic level; And the servo channel comprises a Viterbi detector that excludes state transitions that are excluded from the predetermined binary sequence. However, this feature is well known in the art as disclosed by Patapoutian et al., wherein it teaches a first group of consecutive bits, the first group having first and second equally sized portions and representing a first logic level, the bits in the first portion each having a second logic level (Pat. No. 5, 661, 760; Col. 3, Lines 55-58. Patapoutian et al. teaches a  $\frac{1}{4}$  coding scheme that codes binary ones into "--++" and binary zeros into "++--". It is inherent that if a sequence of for example "1011" ("10" being a first logic level and "11" being a second logic level) will be encoded into "--++++----++--++", having a first and second equally sized portion in the first group ("--++" and "++--") having a second logic level ("1") and a third logic level ("0")) and a Viterbi detector operable to receive a signal that represents a binary sequence (Pat.

Art Unit: 2651

No. 5, 661, 760; See Abstract). It would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to modify the combination's invention with the teaching of Leis et al. in order to receive a binary sequence (Pat. No. 5, 661, 760; See Abstract).

***Allowable Subject Matter***

7. Claims 8 and 18 objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

***Response to Arguments***

8. Applicant's arguments with respect to claims 1-35 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

9. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Blackborow et al. (US Patent No. 5, 062, 262), Machado et al. (US Patent No. 5, 255, 136), Jeppson (US Patent No. 6, 122, 131).

10. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Glenda P. Rodriguez whose telephone number is (703) 305-8411. The examiner can normally be reached on Monday thru Thursday: 7:00-5:00; alternate Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sinh Tran can be reached on (703) 305-4040. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.



Art Unit: 2651

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

gpr  
November 1, 2004.

  
**SINH TRAN**  
**PRIMARY EXAMINER**